



Docket No.: M4065.0317/P317-B  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
David J. Corisis

Application No.: 10/043,104

Confirmation No.: 7226

Filed: January 14, 2002

Art Unit: 2823

For: SEMICONDUCTOR PACKAGE

Examiner: Brook Kebede

**APPLICANT'S COMMENTS ON THE EXAMINER'S  
STATEMENT OF REASONS FOR ALLOWANCE**

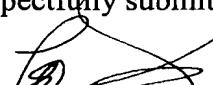
MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In the Notice of Allowance dated June 2, 2005, the Examiner stated reasons for allowance. Applicant agrees in part with the stated reasons for allowance, insofar as the prior art does not teach or suggest the features noted by the Examiner. However, Applicant believes that other aspects of the claimed invention are also not taught or suggested by the prior art of record. Thus, the stated reasons for allowance should interpreted as highlighting only some of the differences between prior art and the claimed invention. As usual, the scope of the claims should be based on the totality of the language of the allowed claims.

Dated: August 23, 2005

Respectfully submitted,

By   
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